

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	( semiconductor and device and comprising and interconnection and board and having and first and second and surfaces and multilevel and insulating and resin and layer and therein and least and one and external and electrode and pad and buried).clm.	US-PGPUB	OR	ON	2006/12/23 20:04